



# Designing Chips with CHIPS: West Coast Pre-silicon Summit

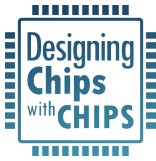
## 3 November 2023

### Qualcomm, San Diego, CA

## Preliminary Agenda\*

(\*Timing and speakers may change slightly)

Friday, 3 November 2023		
Time (PT)	Session	Speaker
8:00 am	<b>Breakfast and Networking</b>	
8:30 am	Welcome and Introductions	<ul style="list-style-type: none"> <li>• <a href="#">Kathy Hayashi</a> IEEE Region 6 Director</li> <li>• <a href="#">Deborah Cooper</a> Past President, IEEE-USA</li> <li>• <a href="#">Tim Lee</a> IEEE-USA President Elect 2024</li> </ul>
8:45 am	Keynote: CHIPS Design Perspective	<ul style="list-style-type: none"> <li>• <a href="#">Dr. PR. "Chidi" Chidambaram</a> Vice President of Engineering and Fellow, Qualcomm</li> </ul>
9:05 am	Keynote: Industry Perspective	<ul style="list-style-type: none"> <li>• <a href="#">Shari Liss</a>, Executive Director SEMI Foundation (remote)</li> </ul>
9:25 am	Fireside Chat: Industry Implications	<ul style="list-style-type: none"> <li>• <a href="#">Deirdre Hanford</a> Chief Security Officer Synopsys</li> <li>• <a href="#">Tim Lee</a></li> <li>• IEEE-USA President Elect 2024</li> </ul>
<b>Perspectives on Chips and CHIPS</b>		
10:00 am	Perspectives from the Engineering Community	<ul style="list-style-type: none"> <li>• <a href="#">Jennifer Fong</a>, moderator</li> <li>• <a href="#">Dr. Andrew Kahng</a> University of California, San Diego</li> <li>• <a href="#">Michelle Thompson</a> Chair, IEEE San Diego Section Open Research Institute, Inc.</li> <li>• <a href="#">Wilbur Luo</a>, Cadence</li> </ul>
10:45 am	<b>Networking Break</b>	
11:00 am	Perspective from Manufacturing	<ul style="list-style-type: none"> <li>• <a href="#">Dr. Ted Cacouris</a> Head of Product Development ASML Cymer</li> </ul>
11:30 am	View from Sacramento: GO-Biz	<ul style="list-style-type: none"> <li>• <a href="#">Michael Karavolias</a> GO-Biz</li> <li>• <a href="#">Christopher Earl</a> California Office of Small Business Advocate</li> <li>• <a href="#">Robert Meyer</a> Director of Economic Development Employment Training Panel</li> </ul>



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		<ul style="list-style-type: none"><li>• <a href="#">Eileen Sánchez</a> CASCADE Program Director California Office of Planning and Research</li></ul>
12:30 pm	<b>Lunch</b>	
1:15 pm	Workforce Initiatives	Introduction: <a href="#">Dr. Kathleen Kramer</a> , IEEE President-Elect 2024 <ul style="list-style-type: none"><li>• <a href="#">Dr. Sandeep Gupta</a> University of Southern California</li><li>• <a href="#">Falan Yinug</a> Qualcomm</li></ul>
2:00 pm	What is Coming Next in California?	<ul style="list-style-type: none"><li>• <a href="#">Dr. C.K. Ken Yang</a> University of California, Los Angeles</li><li>• <a href="#">Dr. Andrea Belz</a> University of Southern California</li></ul>
3:00 pm	<b>Networking Break</b>	
3:30 pm	Keynote: Update from CHIPS.gov and NAPMP	<ul style="list-style-type: none"><li>• <a href="#">Dr. Subramanian Iyer</a> Director, National Advanced Packaging Manufacturing Program</li></ul>
4:15 pm	Closing Remarks	<ul style="list-style-type: none"><li>• <a href="#">Dr. Albert Pisano</a> Dean, Jacobs School of Engineering University of California, San Diego</li></ul>
5:00 pm	<b>Wrap-Up</b>	